

Insights: News Releases

Ming Choy Joins Kilpatrick Townsend in Silicon Valley

May 18, 2017

MENLO PARK (May 18) – Kilpatrick Townsend & Stockton announced today the addition of **Ming Choy** to the firm's Silicon Valley office as an associate in the **Electrical Engineering and Software Team** in the firm's internationally recognized Intellectual Property Department.

Mr. Choy focuses his practice on patent preparation, prosecution, and related counseling in a variety of technologies including analog and mixed signal integrated circuits and systems, very large-scale digital integrated circuits (VLSI), unmanned aerial vehicles & transportation systems, nanoimprinting technology, and computer processor & memory system architectures.

Before joining the firm, Mr. Choy was an associate at an international intellectual property firm in Palo Alto. Prior to launching his legal career, he served as a Software Engineer and Analog Circuit Designer at Intel Corporation.

Mr. Choy earned his J.D., cum laude, from Santa Clara University School of Law. He received his M.S. in Electrical Engineering from Stanford University and his B.S. in Electrical Engineering and Computer Science at the University of California, Berkeley.

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